

	Document ID	Issue Date	Pages	Title	Current OR
1	US 20060227886 A1	20061012	20	Advanced receiver with sliding window block linear equalizer	375/260
2	US 20060209974 A1	20060921	26	Propagation path estimating method and apparatus	375/260
3	US 20060182193 A1	20060817	31	Technique for adaptive equalization in band-limited high data rate communication over fading dispersive channels	375/267
4	US 20060171451 A1	20060803	22	Adaptive equalizer with a dual-mode active taps mask generator and a pilot reference signal amplitude control unit	375/232
5	US 20060114974 A1	20060601	16	Normalized least mean square chip-level equalization advanced diversity receiver	375/148
6	US 20060109897 A1	20060525	23	FFT accelerated iterative MIMO equalizer receiver architecture	375/232
7	US 20060098766 A1	20060511	17	Method and apparatus for estimating the step-size of an adaptive equalizer	375/346
8	US 20050271135 A1	20051208	25	Receiving device and analog-to-digital conversion device	375/232
9	US 20050175128 A1	20050811	14	Interference-free lms-based adaptive asynchronous receiver	375/350
10	US 20050175112 A1	20050811	17	Time domain equalization using frequency domain operations	375/260
11	US 20050169361 A1	20050804	28	Computation of decision feedback equalizer coefficients with constrained feedback tap energy	375/233
12	US 20050111540 A1	20050526	8	Adaptive equalizer operating at a sampling rate asynchronous to the data rate	375/232
13	US 20050041760 A1	20050224	22	Computation of decision feedback equalizer coefficients with constrained feedback tap energy	375/340
14	US 20050036576 A1	20050217	78	Multi-pair gigabit ethernet transceiver	375/348
15	US 20040240595 A1	20041202	21	Equalizer for communication over noisy channels	375/350
16	US 20040233981 A1	20041125	24	Method and apparatus for predistorting data	375/229
17	US 20040156461 A1	20040812	87	Multi-pair gigabit ethernet transceiver	375/350

	<b>Inventor</b>
<b>1</b>	Li; Bin et al.
<b>2</b>	Yoshida; Makoto
<b>3</b>	Monsen; Peter
<b>4</b>	Pietraski; Philip J. et al.
<b>5</b>	Zeira; Ariela et al.
<b>6</b>	Guo; Yuanbin et al.
<b>7</b>	Pietraski; Philip J. et al.
<b>8</b>	Shida, Yasunari
<b>9</b>	Modrie, David et al.
<b>10</b>	Pisoni, Fabio et al.
<b>11</b>	Yousef, Nabil R. et al.
<b>12</b>	Modrie, David et al.
<b>13</b>	Yousef, Nabil R.
<b>14</b>	Agazzi, Oscar E. et al.
<b>15</b>	Raphaeli, Dan
<b>16</b>	Porter, John David et al.
<b>17</b>	Agazzi, Oscar E. et al.

	Document ID	Issue Date	Pages	Title	Current OR
18	US 20040091037 A1	20040513	22	Adaptive signal equalizer with adaptive error timing and precursor/postcursor configuration control	375/232
19	US 20040091036 A1	20040513	22	Adaptive signal equalizer with adaptive error timing and precursor/postcursor configuration control	375/232
20	US 20040071243 A1	20040415	87	Multi-pair gigabit ethernet transceiver	375/348
21	US 20040071242 A1	20040415	87	Multi-pair gigabit ethernet transceiver	375/348
22	US 20040071206 A1	20040415	34	Digital filter adaptively learning filter coefficient	375/232
23	US 20040047409 A1	20040311	15	Adaptive transmit equalizer	375/232
24	US 20040028159 A1	20040212	40	Receivers, methods, and computer program products for an analog modem that receives data signals from a digital modem	375/350
25	US 20040005010 A1	20040108	27	Channel estimator and equalizer for OFDM systems	375/260
26	US 20030235244 A1	20031225	65	Method and apparatus for performing adaptive filtering	375/232
27	US 20030185322 A1	20031002	37	Digital radio receiver	375/350
28	US 20030112887 A1	20030619	54	Method and system for implementing weighted vector error echo cancellers	375/285
29	US 20030112861 A1	20030619	52	Method and system for adaptively training time domain equalizers	375/232
30	US 20030095592 A1	20030522	11	ZF-based adaptive asynchronous receiver	375/232
31	US 20030063680 A1	20030403	16	Per-bin DFE for advanced OQAM-based multi-carrier wireless data transmission systems	375/260
32	US 20030048840 A1	20030313	9	Adaptive equalizer	375/232
33	US 20020146062 A1	20021010	10	MULTI-USER DETECTION USING A FINITE-IMPULSE-RESPONSE MATRIX FILTER	375/144

	<b>Inventor</b>
18	Balasubramonian, Venugopal et al.
19	Balasubramonian, Venugopal et al.
20	Agazzi, Oscar E. et al.
21	Agazzi, Oscar E. et al.
22	Takatsu, Motomu
23	Lee, Patrick J. et al.
24	Abdelilah, Youssef et al.
25	He, Ping et al.
26	Pessoa, Lucio F. C. et al.
27	Takahashi, Tsutomu
28	Sang, Tzu Hsien et al.
29	Erdogan, Alper Tunga et al.
30	Bergmans, Johannes Wilhemus Maria et al.
31	Nedic, Slobodan et al.
32	Ling, Stanley K. et al.
33	Bachu, Raja S. et al.

	Document ID	Issue Date	Pages	Title	Current OR
34	US 20020131537 A1	20020919	19	Approach for processing data received from a communications channel to reduce noise power and optimize impulse response length to reduce inter-symbol interference and inter-channel interference	375/350
35	US 20020122499 A1	20020905	26	Method and apparatus for error reduction in an orthogonal modulation system	375/260
36	US 20020024994 A1	20020228	18	Method for improved channel impulse response estimation in TDMA systems	375/231
37	US 20010036237 A1	20011101	14	Approach for processing data received from a communications channel in finite precision arithmetic applications	375/350
38	US 7139343 B2	20061121	36	Digital radio receiver	375/350
39	US 7099384 B1	20060829	23	Method and apparatus for time-division power assignments in a wireless communication system	375/229
40	US 7050513 B1	20060523	21	Block based channel tracking using weighted recursive least squares	375/341
41	US 7023938 B1	20060404	27	Receiver for discrete multitone modulated signals having window function	375/350
42	US 7010030 B2	20060307	17	Software definable block adaptive decision feedback equalizer	375/233
43	US 7006564 B2	20060228	9	Adaptive equalizer	375/232
44	US 6996199 B2	20060207	19	Approach for processing data received from a communications channel to reduce noise power and optimize impulse response length to reduce inter-symbol interference and inter-channel interference	375/350
45	US 6983030 B2	20060103	18	Approach for processing data received from a communications channel in finite precision arithmetic applications	375/350
46	US 6954495 B2	20051011	8	Optimization of channel equalizer	375/233
47	US 6937648 B2	20050830	20	Equalizer for communication over noisy channels	375/229
48	US 6904110 B2	20050607	183	Channel equalization system and method	375/350

	<b>Inventor</b>
<b>34</b>	Storm, Andrew et al.
<b>35</b>	Kannan, Anand et al.
<b>36</b>	Piirainen, Olli et al.
<b>37</b>	Storm, A. et al.
<b>38</b>	Takahashi; Tsutomu
<b>39</b>	Jalali; Ahmad et al.
<b>40</b>	Yakhnich; Evgeny
<b>41</b>	Kapoor; Samir et al.
<b>42</b>	Vaidyanathan; Krishnamurthy
<b>43</b>	Ling; Stanley K. et al.
<b>44</b>	Storm; Andrew et al.
<b>45</b>	Storm; Andrew et al.
<b>46</b>	Piirainen; Olli
<b>47</b>	Raphaeli; Dan
<b>48</b>	Trans; Francois et al.

	Document ID	Issue Date	Pages	Title	Current OR
49	US 6826242 B2	20041130	30	Method for whitening colored noise in a communication system	375/350
50	US 6819514 B1	20041116	32	Adaptive equalization and interpolated timing recovery in a sampled amplitude read channel for magnetic recording	360/65
51	US 6816558 B2	20041109	6	Channel impulse response estimation using received signal variance	375/316
52	US 6778598 B2	20040817	11	Method for modifying the channel impulse response in TDMA systems	375/231
53	US 6775322 B1	20040810	20	Equalizer with adaptive pre-filter	375/232
54	US 6704355 B1	20040309	11	Method and apparatus to enhance timing recovery during level learning in a data communication system	375/231
55	US 6665336 B2	20031216	37	MODEMS, METHODS, AND COMPUTER PROGRAM PRODUCTS FOR SELECTING AN OPTIMUM DATA RATE USING ERROR SIGNALS REPRESENTING THE DIFFERENCE BETWEEN THE OUTPUT OF AN EQUALIZER AND THE OUTPUT OF A SLICER OR DETECTOR	375/225
56	US 6661837 B1	20031209	40	MODEMS, METHODS, AND COMPUTER PROGRAM PRODUCTS FOR SELECTING AN OPTIMUM DATA RATE USING ERROR SIGNALS REPRESENTING THE DIFFERENCE BETWEEN THE OUTPUT OF AN EQUALIZER AND THE OUTPUT OF A SLICER OR DETECTOR	375/227
57	US 6631160 B2	20031007	17	Method for improved channel impulse response estimation in TDMA systems	375/231
58	US 6603811 B1	20030805	9	Low complexity frequency domain equalizer having fast re-lock	375/232
59	US 6535552 B1	20030318	15	Fast training of equalizers in discrete multi-tone (DMT) systems	375/231
60	US 6516025 B1	20030204	18	High-speed upstream modem communication	375/222

	<b>Inventor</b>
49	Ojard; Eric et al.
50	Behrens; Richard T. et al.
51	Piirainen; Olli et al.
52	Happonen; Aki et al.
53	Zangi; Kambiz C. et al.
54	Lai; Yhean-Sen
55	Abdelilah; Youssef et al.
56	Abdelilah; Youssef et al.
57	Piirainen; Olli et al.
58	Dobson; William Kurt et al.
59	Pessoa; Lucio
60	Warke; Nirmal C. et al.



	Document ID	Issue Date	Pages	Title	Current OR
61	US 6459729 B1	20021001	12	Method and apparatus for improved channel equalization and level learning in a data communication system	375/231
62	US 6408022 B1	20020618	19	Equalizer for use in multi-carrier modulation systems	375/230
63	US 6384858 B1	20020507	29	Suppression of co-channel NTSC interference artifacts when extracting training signal for a DTV receiver equalizer	348/21
64	US 6360369 B1	20020319	14	Interference tolerant modem	725/111
65	US 6301298 B1	20011009	13	Adaptive equalizer with complex signal regeneration and method of operation	375/232
66	US 6259729 B1	20010710	20	Method of and an apparatus for training tap coefficients of an adaptive equalizer	375/231
67	US 6208481 B1	20010327	34	Gain and phase constrained adaptive equalizing filter in a sampled amplitude read channel for magnetic recording	360/65
68	US 6185251 B1	20010206	18	Equalizer for use in multi-carrier modulation systems	375/231
69	US 5999355 A	19991207	33	Gain and phase constrained adaptive equalizing filter in a sampled amplitude read channel for magnetic recording	360/65
70	US 5805480 A	19980908	20	Rotationally predictive adaptive filter	708/322
71	US 5802118 A	19980901	43	Sub-sampled discrete time read channel for computer storage systems	375/350
72	US 5777692 A	19980707	14	Receiver based methods and devices for combating co-channel NTSC interference in digital transmission	348/725
73	US 5710793 A	19980120	29	Error signal quantization method and hardware for mixed blind and decision directed equalization	375/232
74	US 5642382 A	19970624	20	Fir filters with multiplexed inputs suitable for use in reconfigurable adaptive equalizers	375/232
75	US 5590154 A	19961231	8	Equalizer circuit and a method for equalizing a continuous signal	375/229
76	US 5557644 A	19960917	16	Signal demodulating and decoding apparatus and signal demodulating and decoding method	375/340

	<b>Inventor</b>
61	Lai; Yhean-Sen
62	Fertner; Antoni
63	Limberg; Allen LeRoy
64	Mahoney; Paul F.
65	Kuntz; Thomas L. et al.
66	Seki; Katsutoshi
67	Spurbeck; Mark S. et al.
68	Fertner; Antoni
69	Behrens; Richard T. et al.
70	Greenberg; Craig B.
71	Bliss; William G. et al.
72	Ghosh; Monisha
73	Greenberg; Craig Bradley
74	Juan; Yujen
75	Forni; William R. et al.
76	Kuwabara; Takashi

	Document ID	Issue Date	Pages	Title	Current OR
77	US 5440583 A	19950808	13	Decision feedback equalizer with second-order recursive filter controlled with two feedback coefficients	375/233
78	US 5432816 A	19950711	21	System and method of robust sequence estimation in the presence of channel mismatch conditions	375/232
79	US 5293402 A	19940308	11	Wideband digital equalizers for subscriber loops	375/233
80	US 5243624 A	19930907	14	Method and apparatus for updating coefficients in a complex adaptive equalizer	375/232
81	US 5142552 A	19920825	5	Method and apparatus for analog D.C. offset cancellation	375/232

	<b>Inventor</b>
<b>77</b>	Koike; Shin'ichi
<b>78</b>	Gozzo; Francesco
<b>79</b>	Crespo; Pedro M. et al.
<b>80</b>	Paik; Woo H. et al.
<b>81</b>	Tzeng; Jeremy et al.